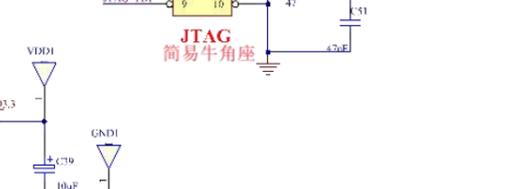
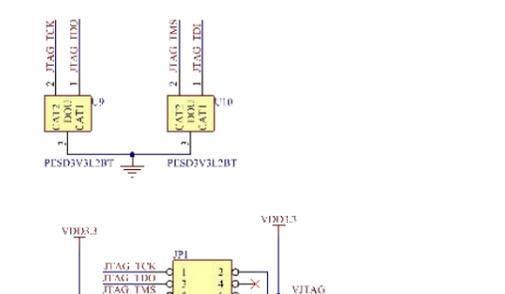
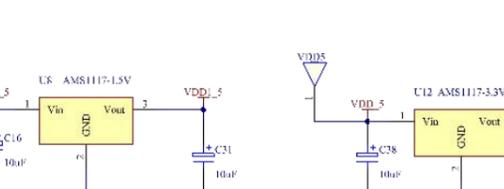
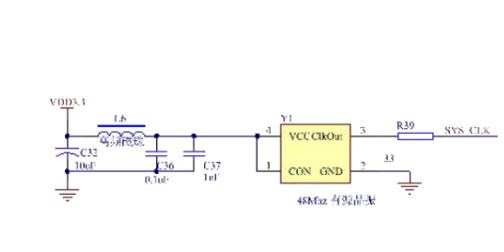
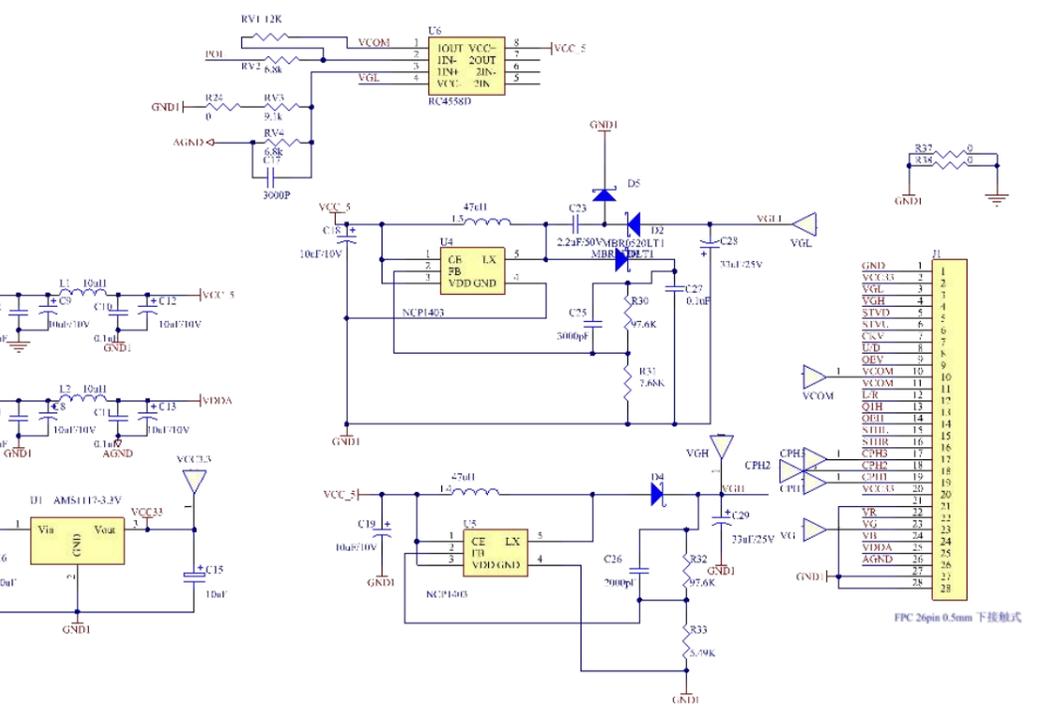
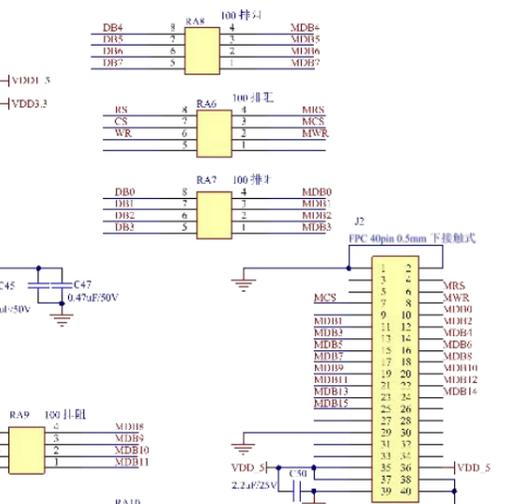
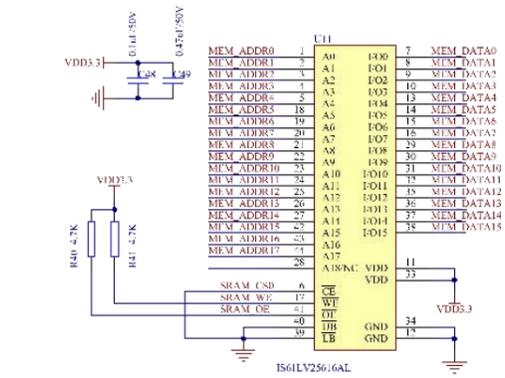
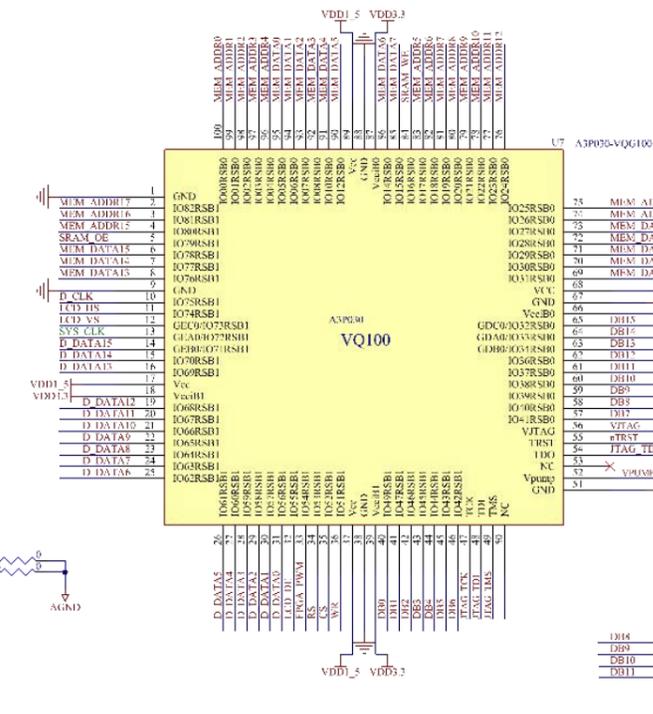
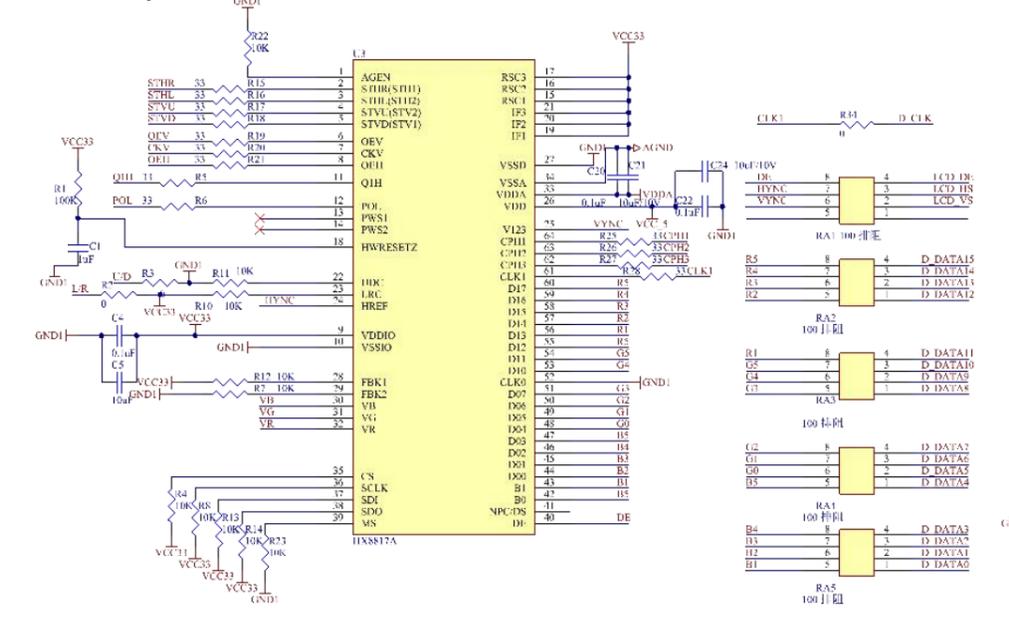
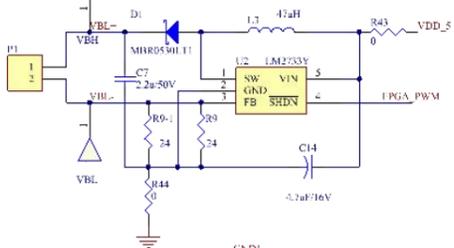
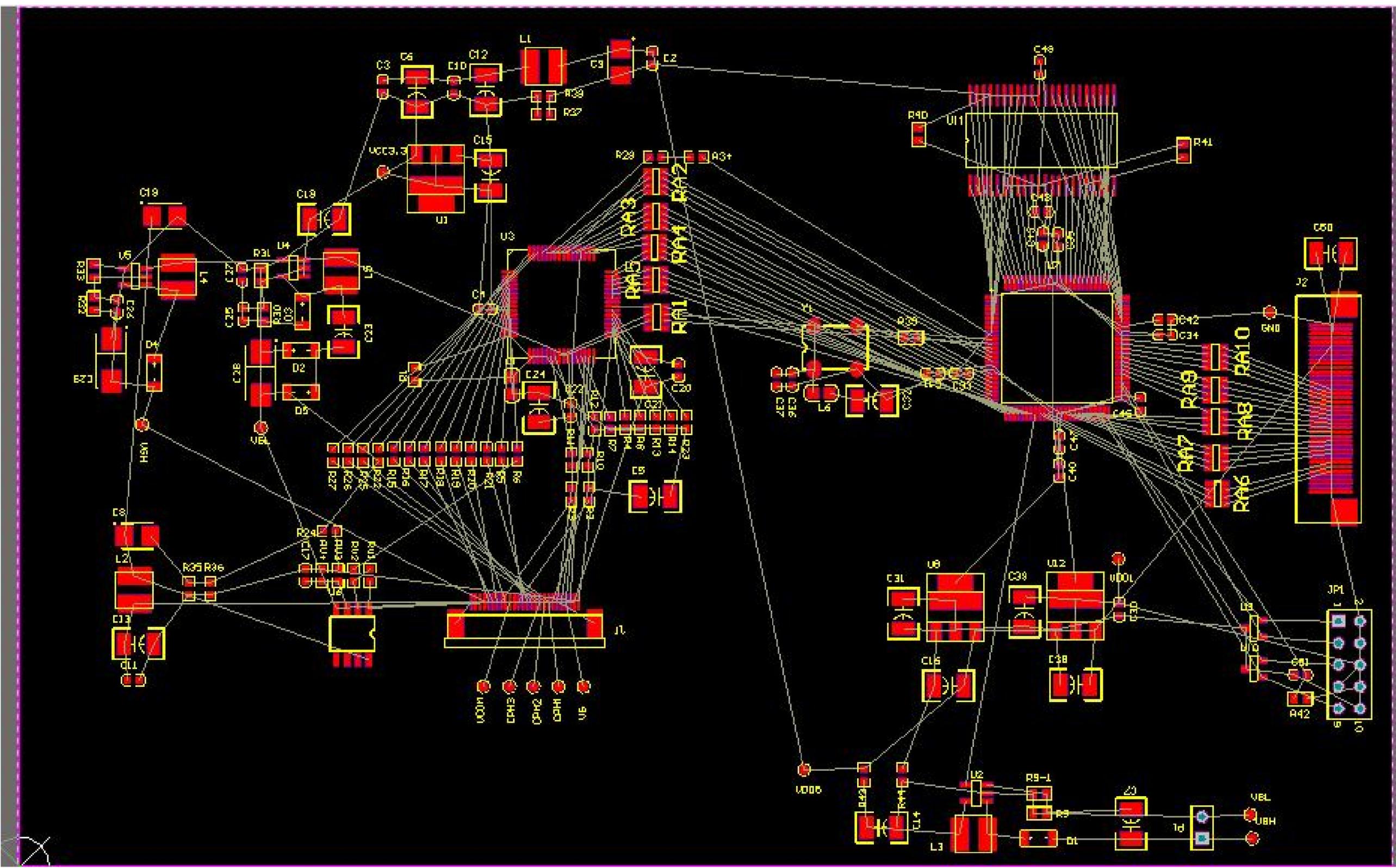


LCD背光POWER

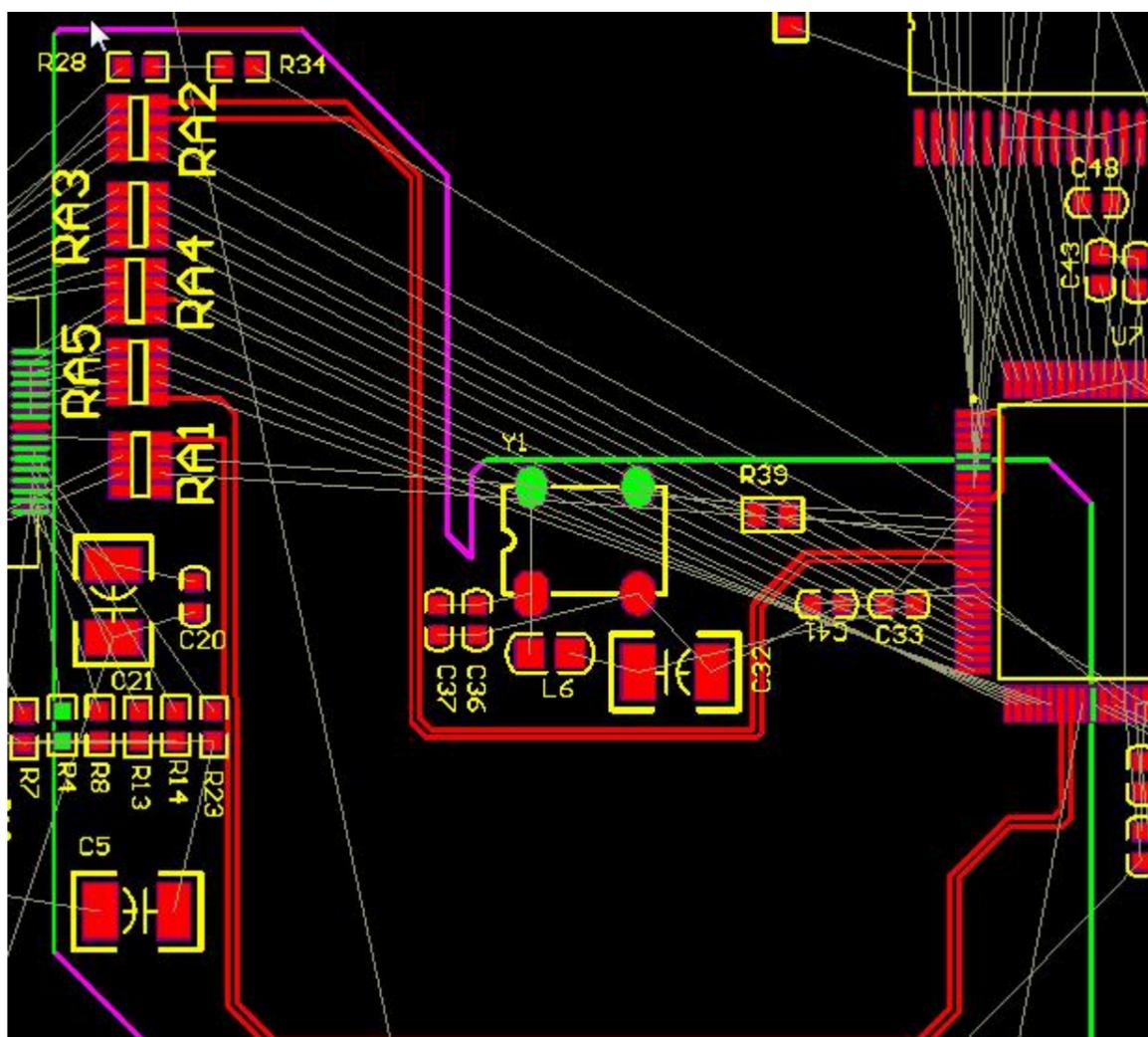




The screenshot displays a PCB design software interface. On the left, a 'Projects' panel shows a workspace named 'Workspace1.DsnWrk' and a file tree with folders for 'Source Documents' and 'Free Documents'. The main area shows a detailed PCB layout with various components like resistors (R1, R2, etc.), capacitors (C1, C2, etc.), and integrated circuits (U1, U2, etc.) connected by a network of red and blue traces. A 'Messages' window is open in the center, displaying a log of routing progress.

Class	Document	Source	Message	Time	Date	N..
Sit...	A3P030_48...	Situs	Starting Fan out to Plane	19:31:15	2017/5/25	3
Sit...	A3P030_48...	Situs	Completed Fan out to Plane in 0 Seconds	19:31:15	2017/5/25	4
Sit...	A3P030_48...	Situs	Starting Memory	19:31:15	2017/5/25	5
Ro...	A3P030_48...	Situs	44 of 428 connections routed (10.28%) in 3...	19:31:16	2017/5/25	6
Sit...	A3P030_48...	Situs	Completed Memory in 1 Second	19:31:16	2017/5/25	7
Sit...	A3P030_48...	Situs	Starting Layer Patterns	19:31:16	2017/5/25	8
Ro...	A3P030_48...	Situs	233 of 428 connections routed (54.44%) in ...	19:31:22	2017/5/25	9
Sit...	A3P030_48...	Situs	Completed Layer Patterns in 5 Seconds	19:31:22	2017/5/25	10
Sit...	A3P030_48...	Situs	Starting Main	19:31:22	2017/5/25	11
Ro...	A3P030_48...	Situs	341 of 428 connections routed (79.67%) in ...	19:32:24	2017/5/25	12

自动布线是不行的。



手动布线按我们提供的文件手动布线完成。

